

TRANSLATION OF AN ELECTRONIC INTEGRATED CIRCUIT DESIGN INTO
HARDWARE

ABSTRACT

Translation of an electronic design of an integrated circuit into circuit description language is described. In an example, a connection among circuit descriptions representing behavior of circuit elements in the electronic design is identified. The connection is associated with an identifier. The electronic design is then translated into a circuit description language representation, where the connection is implemented within the circuit description language representation using the identifier. In another example, an implicit circuit description representing behavior of circuit elements within the electronic design are identified. Explicit circuit descriptions within the electronic design are augmented with an additional circuit description. The electronic design is then translated into a circuit description language representation.